

ABSTRACT OF THE DISCLOSURE

Described is a method for the clock-pulse selection in a baseband combiner of a space-diversity receiver. The method provides for driving the clock recovery circuit by means of the main signal, filtered and sampled, or by the diversity signal, filtered and sampled as well, and it is characterized by calculating the average power of the equalized main signal and the average power of the equalized diversity signal; and by driving the clock recovery circuit by means of the signal chosen according to a comparison between the average powers of the signals at the output of the equalizers. Advantageously, the invention provides for a hysteresis on the power comparison and guarantees a soft change with respect to the selection performed at the preceding time of processing.